

OC-768 MULTIPLEXED CLOCK OSCILLATOR

Model FO-CL-20-00

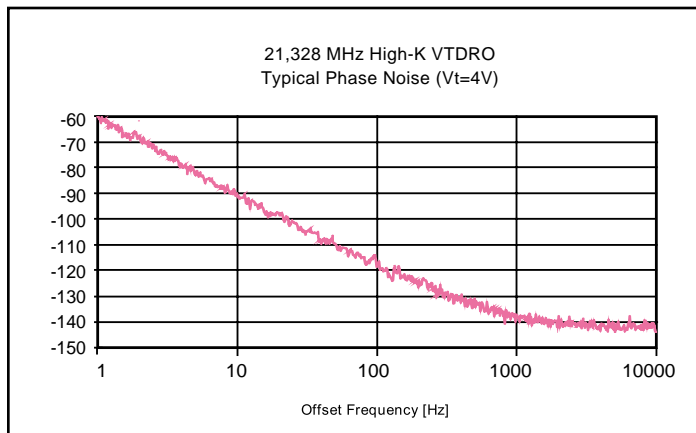
Applications: Designed for critical timing applications required in multiplexed SONET/SDH OC-768 systems.

Features:

- ◆ Ultra-Stable DRO Technology
- ◆ Lowest Phase Noise / Jitter
- ◆ Minimal DC Power Dissipation
- ◆ Low Cost SMT Construction

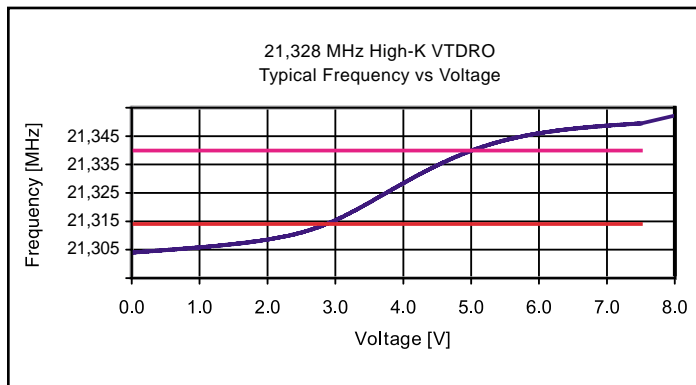
Phase Noise, Max (-30° to +70°C):

1 kHz offset	-57 dBc/Hz
10 kHz offset	-87 dBc/Hz
100 kHz offset	-115 dBc/Hz
1 MHz offset	-135 dBc/Hz



Output Characteristics:

Typical Frequency:	19.5 to 22 GHz
Typical Tuning Range:	20 to 40 MHz
Tuning Voltage:	3 to 5 Volts (High K) 2 to 10 Volts (Low K)
Modulation Sens:	12 MHz/V Typ. (High K) 2 MHz/V Typ. (Low K)
Output Power:	+18 dBm, Typ. (Other power levels available)



Connectors:

RF: "K" Connector
(Alternative connectors available)

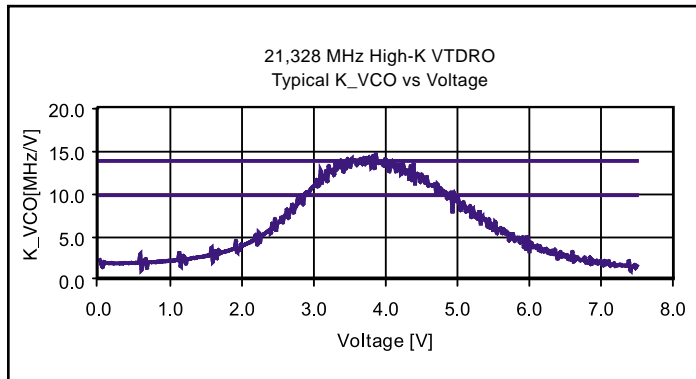
DC: Solder Pin

DC Power:

+8 to +12V @ 190 mA

-8 to -12V @ 15 mA

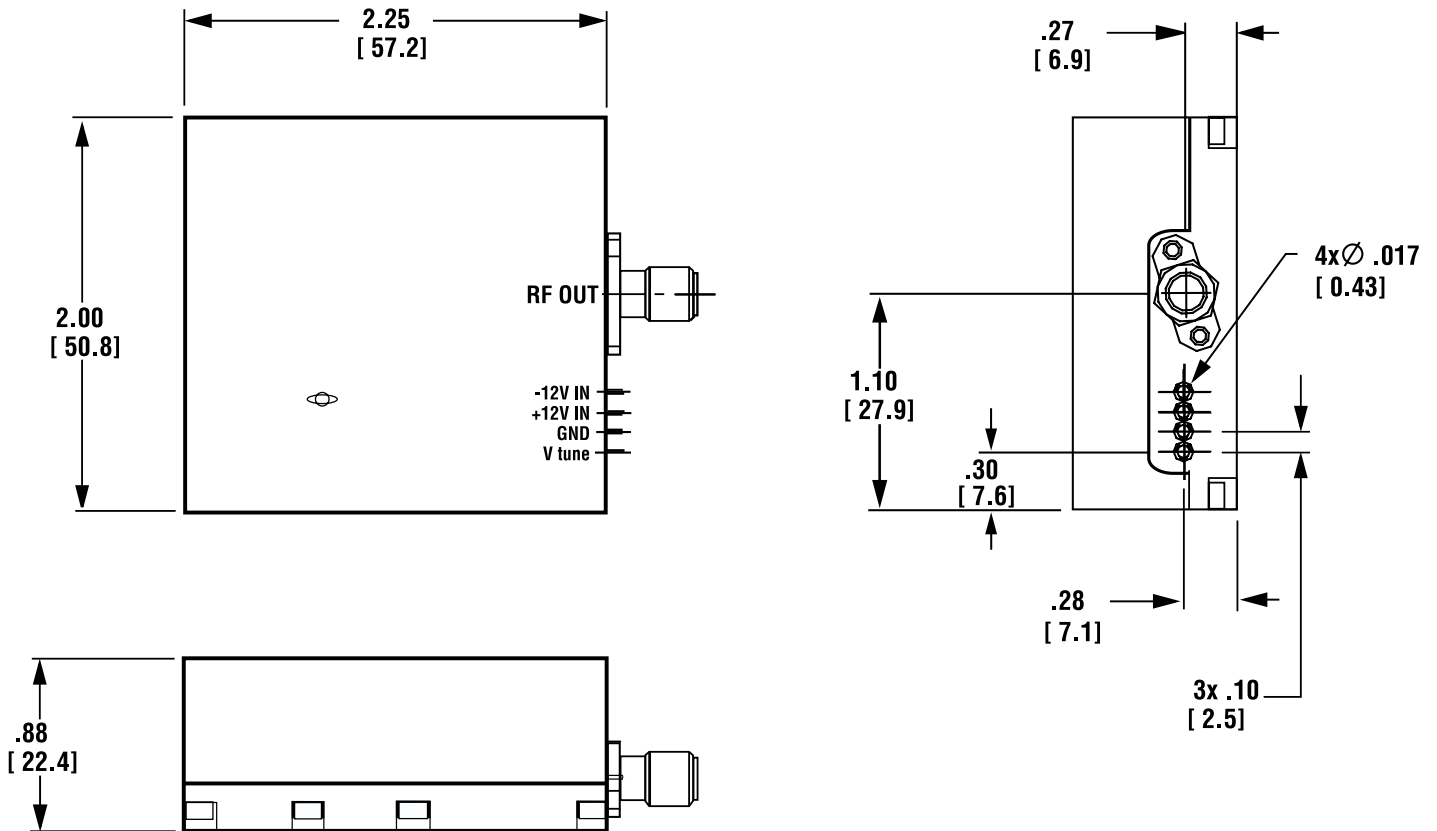
Internally Regulated



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Outline Drawing



Contact Narda for your custom requirements.

L-3 Communications has patent applications pending which protects this and related fiber optic products.

These products may also be protected by other intellectual property rights including trademarks and copyrights.